

LCM ENGINEERING SPECIFICATION

The Below Models Reference this spec
LC500DUG-JFR1-7R1-B;

*MODEL	LC500DUG
SUFFIX	JFR1
Update	Oct.19, 2012

- () **Preliminary Specification**
(●) **Final Specification**

Engineering Specification

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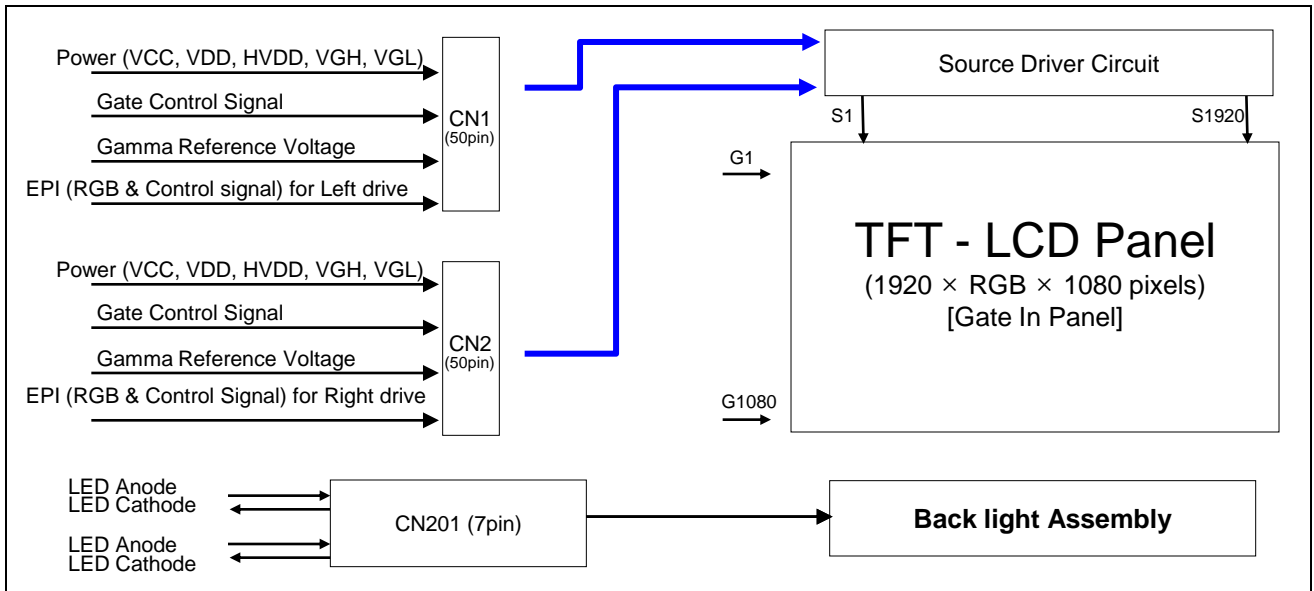
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1. General Description

The LC500 DUG is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system . The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 49.5 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a **8-bit** gray scale signal for each dot. Therefore, it can present a palette of more than **1.67M(ure) colors**. It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



General Features

Active Screen Size	49.50 inches(1257.31mm) diagonal
Outline Dimension	1122.6(H) × 647.8(V) X 38.0(B)/49.0 mm(D) (Typ.)
Pixel Pitch	0.57075 mm x 0.57075 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	8bit, 16.7 Million colors
Luminance, White	300 cd/m ² (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178 (Min.), U/D 178 (Min.))
Power Consumption	Total 95.5W (Typ.) [Logic= 6.7(TBD)W, LED Backlight=88.8(TBD)W (IF_cathode=400mA)
Weight	10.0 kg
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-reflection treatment of the front polarizer (Haze 1%)

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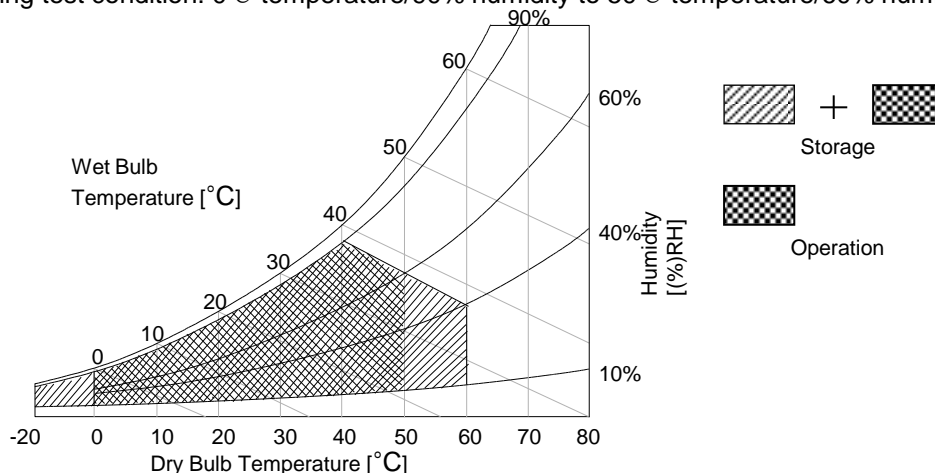
2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or **permanent** damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value		Unit	Note
			Min	Max		
Logic&EPI Power Voltage		VCC	-0.5	+2.2	V _{DC}	1
Gate High Voltage		VGH	+18.0	+30.0	V _{DC}	
Gate Low Voltage		VGL	-8.0	-4.0	V _{DC}	
Source D-IC Analog Voltage		VDD	-0.3	+18.0	V _{DC}	
Gamma Ref. Voltage (Upper)		VGMH	½VDD-0.5	VDD+0.5	V _{DC}	
Gamma Ref. Voltage (Low)		VGML	-0.3	½ VDD+0.5	V _{DC}	
LED Input Voltage	Forward Voltage	V _F	-	+130	V _{DC}	
Panel Front Temperature		T _{SUR}	-	+68	°C	4
Operating Temperature		T _{OP}	0	+50	°C	2,3
Storage Temperature		T _{ST}	-20	+60	°C	
Operating Ambient Humidity		H _{OP}	10	90	%RH	
Storage Humidity		H _{ST}	10	90	%RH	

- Note
1. Ambient temperature condition ($T_a = 25 \pm 2 \text{ }^\circ\text{C}$)
 2. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be Max 39 °C and no condensation of water.
 3. Gravity mura can be guaranteed below 40°C condition.
 4. The maximum operating temperature is based on the test condition that the surface temperature of display area is less than or equal to 68 °C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68 °C. The range of operating temperature may degrade in case of improper thermal management in final product design.
 5. The storage test condition: -20°C temperature/90% humidity to 60°C temperature/40% humidity ; the operating test condition: 0°C temperature/90% humidity to 50°C temperature/60% humidity.



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3. Electrical Specifications

3-1. Electrical Characteristics

It requires several power inputs. The VCC is the basic power of LCD Driving power sequence, Which is used to logic power voltage of Source D-IC and GIP.

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Condition	MIN	TYP	MAX	Unit	notes
Logic & EPI Power Voltage	VCC	-	1.62	1.8	1.98	VDC	
Logic High Level Input Voltage	V _{IH}	-	1.4	-	VCC	VDC	
Logic Low Level Input Voltage	V _{IL}	-	0	-	0.4	VDC	
Source D-IC Analog Voltage	VDD	-	15.82	16.02	16.22	VDC	
Half Source D-IC Analog Voltage	H_VDD	-	7.81	8.01)	8.21	VDC	6
Gamma Reference Voltage	V _{GMH}	(GMA1 ~ GMA9)	H_VDD+0.2V	-	VDD-0.2	VDC	
	V _{GML}	(GMA10 ~ GMA18)	0.2	-	H_VDD-0.2V	VDC	
Common Voltage	Vcom	Reverse	6.72	7.02	7.32	V	
EPI input common voltage	VCM	LVDS Type	0.8	VCC/2	1.3	V	5
EPI input differential voltage	Vdiff	-	150	-	500	mV	
EPI Input eye diagram	Veye	-	90	-	-	mV	
Gate High Voltage	VGH	@ 25°C	27.7	28	28.3	VDC	
		@ 0°C	29.7	30	30.3	VDC	
Gate Low Voltage	VGL	-	-5.2	-5.0	-4.8	VDC	
GIP Bi-Scan Voltage	VGI_P	-	VGL	-	-	VDC	
	VGI_N	-	-	-	VGH	VDC	
GIP Refresh Voltage	VGH even/odd	-	VGL	-	VGH	V	
GIP Start Pulse Voltage	VST	-	VGL	-	VGH	V	
GIP Operating Clock	GCLK	-	VGL	-	VGH	V	
Total Power Current	ILCD	-	-	834	1084	mA	1
Total Power Consumption	PLCD	-	-	6.7	8.7	Watt	1

- notes:
1. The specified current and power consumption are under the V_{LCD}=12V., 25 ± 2°C, f_V=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f_V is the frame frequency.
 2. The above spec is based on the basic model.
 3. All of the typical gate voltage should be controlled within 1% voltage level
 4. Ripple voltage level is recommended under ±5% of typical voltage
 5. In case of EPI signal spec, refer to Fig 2 for the more detail.
 6. HVDD Voltage level is half of VDD and it should be between Gamma9 and Gamma10.

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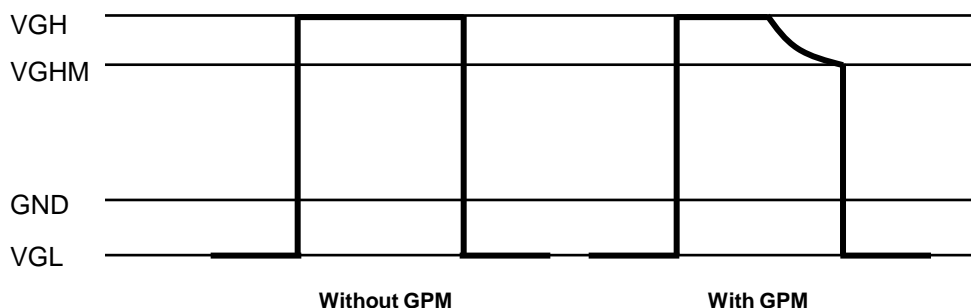


FIG. 1 Gate Output Wave form without GPM and with GPM

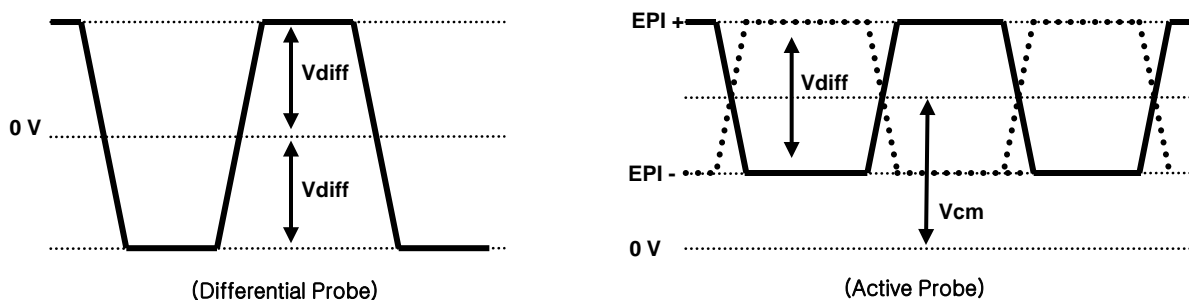


FIG. 2-1 EPI Differential signal characteristics

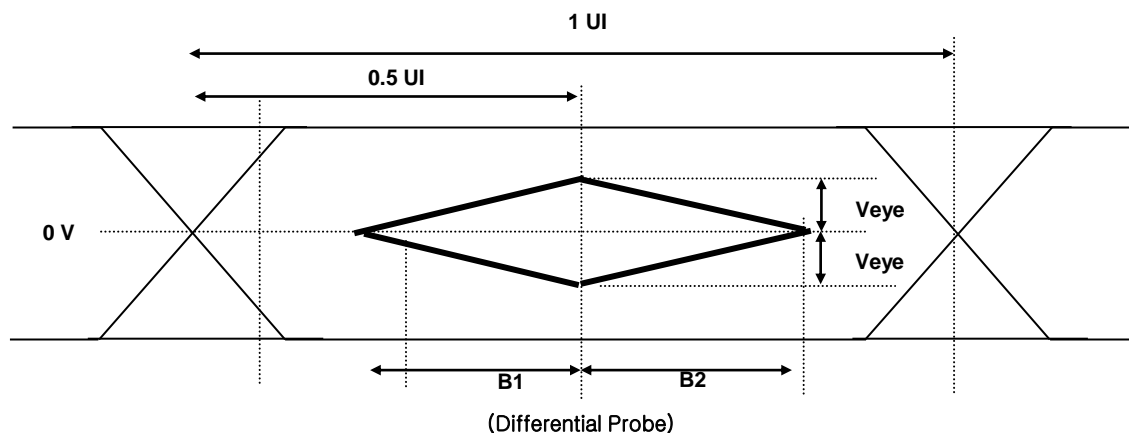


FIG. 2-2 Eye Pattern of EPI Input



FIG. 3 Measure point

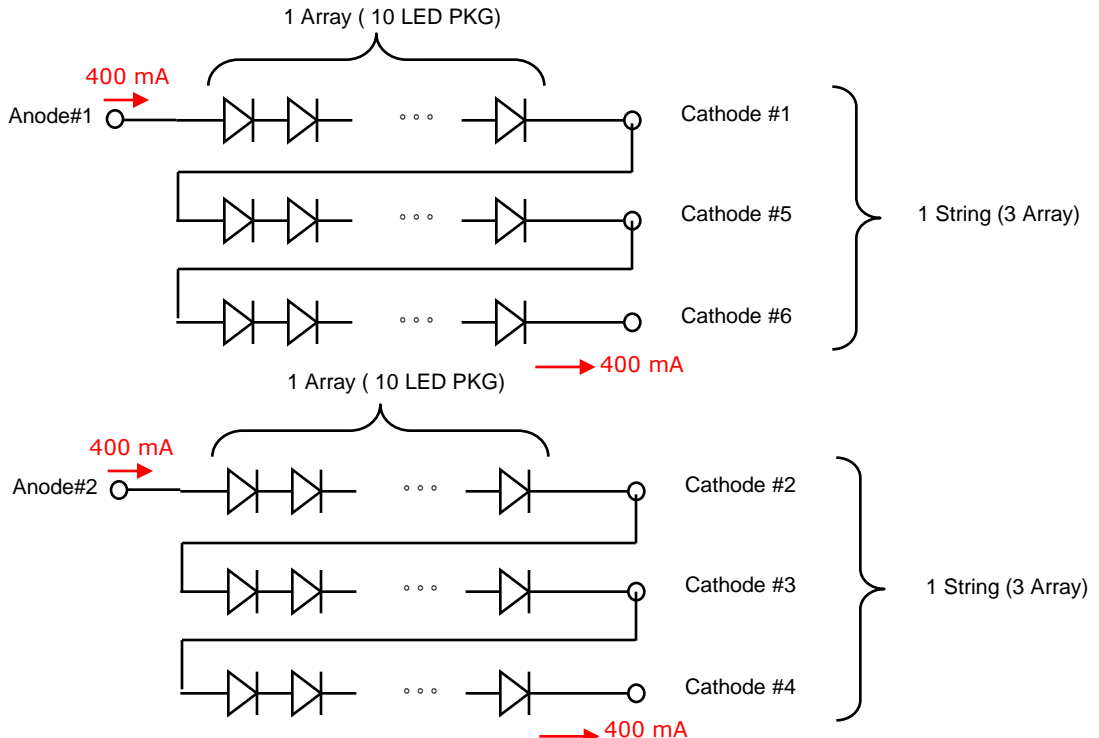
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Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Symbol	Values			Unit	Note
			Min	Typ	Max		
Backlight Assembly :							
Forward Current (one array)	Anode	I _{F (anode)}		400		mAdc	±5%
	Cathode	I _{F (cathode)}		400		mAdc	2, 3
Forward Voltage		V _F	102	111	120	Vdc	4
Forward Voltage Variation		△ V _F			TBD	Vdc	5
Power Consumption		P _{BL}	-	88.8	96	W	6
Burst Dimming Duty		On duty	1		100	%	
Burst Dimming Frequency		1/T	95		182	Hz	8
LED Array : (APPENDIX-V)							
Life Time			30,000			Hrs	7

- Notes :
- The design of the LED driver must have specifications for the LED array in LCD Assembly.
 - The electrical characteristics of LED driver are based on Constant Current driving type.
 - The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED Driver. So, all the parameters of an LED driver should be carefully designed.
 - When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the driver (no lighting, flicker, etc) has never been occurred. When you confirm it, the LCD-Assembly should be operated in the same condition as installed in your instrument.
 - 1. Electrical characteristics are based on LED Array specification.
 - 2. Specified values are defined for a Backlight Assembly. (IBL :6 LED array/LCM)
 - 3. Each LED array has **one** anode terminal and one cathode terminals.

The forward current(I_F) of the anode terminal is 400mA and it supplies 400mA into 1 strings, respectively



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3-2. Interface Connections

This LCD module employs two kinds of interface connection, two 50-pin FFC connector are used for the module electronics.

3-2-1. LCD Module

-LCD Connector (CN1): TF06L-50S-0.5SH (Manufactured by HRS) or Compatible

Table 3-1. MODULE CONNECTOR(CN1) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	LTD_OUT	LTD OUTPUT	26	GND	Ground
2	NC	No Connection	27	EPI2-	EPI Receiver Signal(2-)
3	GCLK1	GIP GATE Clock 1	28	EPI2+	EPI Receiver Signal(2+)
4	GCLK2	GIP GATE Clock 2	29	GND	Ground
5	GCLK3	GIP GATE Clock 3	30	GND	Ground
6	GCLK4	GIP GATE Clock 4	31	EPI1-	EPI Receiver Signal(1-)
7	GCLK5	GIP GATE Clock 5	32	EPI1+	EPI Receiver Signal(1+)
8	GCLK6	GIP GATE Clock 6	33	GND	Ground
9	VGI_N	GIP Bi-Scan (Normal =VGL Rotate = VGH)	34	VCC	Logic & EPI Power Voltage
10	VGI_P	GIP Bi-Scan (Normal =VGH Rotate = VGL)	35	NC	No Connection
11	VGH_ODD	GIP Panel VDD for Odd GATE TFT	36	LOCKOUT3	LOCKOUT3
12	VGH_EVEN	GIP Panel VDD for Even GATE TFT	37	NC	No Connection
13	VGL	GATE Low Voltage	38	GND	Ground
14	VST	VERTICAL START PULSE	39	GMA 18	GAMMA VOLTAGE 18 (Output From LCD)
15	GIP_Reset	GIP Reset	40	GMA 16	GAMMA VOLTAGE 16
16	VCOM_L_FB	VCOM Left Feed-Back Output	41	GMA 15	GAMMA VOLTAGE 15
17	VCOM_L	VCOM Left Input	42	GMA 14	GAMMA VOLTAGE 14
18	GND	Ground	43	GMA 12	GAMMA VOLTAGE 12
19	VDD	Driver Power Supply Voltage	44	GMA 10	GAMMA VOLTAGE 10 (Output From LCD)
20	VDD	Driver Power Supply Voltage	45	GMA 9	GAMMA VOLTAGE 9 (Output From LCD)
21	H_VDD	Half Driver Power Supply Voltage	46	GMA 7	GAMMA VOLTAGE 7
22	GND	Ground	47	GMA 5	GAMMA VOLTAGE 5
23	EPI3-	EPI Receiver Signal(3-)	48	GMA 4	GAMMA VOLTAGE 4
24	EPI3+	EPI Receiver Signal(3+)	49	GMA 3	GAMMA VOLTAGE 3
25	GND	Ground	50	GMA 1	GAMMA VOLTAGE 1(Output From LCD)

Note :

1. Please refer to application note for details.
(GIP & Half VDD & Gamma Voltage setting)

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-LCD Connector (CN1): TF06L-50S-0.5SH (Manufactured by HRS) or Compatible

Table 3-2. MODULE CONNECTOR(CN2) PIN CONFIGURATION

No	Symbol	Description	No	Symbol	Description
1	GMA 1	GAMMA VOLTAGE 1 (Output From LCD)	26	GND	Ground
2	GMA 3	GAMMA VOLTAGE 3	27	EPI1-	EPI Receiver Signal(4-)
3	GMA 4	GAMMA VOLTAGE 4	28	EPI1+	EPI Receiver Signal(4+)
4	GMA 5	GAMMA VOLTAGE 5	29	GND	Ground
5	GMA 7	GAMMA VOLTAGE 7	30	H_VDD	Half Driver Power Supply Voltage
6	GMA 9	GAMMA VOLTAGE 9 (Output From LCD)	31	VDD	Driver Power Supply Voltage
7	GMA 10	GAMMA VOLTAGE 10 (Output From LCD)	32	VDD	Driver Power Supply Voltage
8	GMA 12	GAMMA VOLTAGE 12	33	GND	Ground
9	GMA 14	GAMMA VOLTAGE 14	34	VCOM_R	VCOM Right Input
10	GMA 15	GAMMA VOLTAGE 15	35	VCOM_R_FB	VCOM Right Feed-Back Output
11	GMA 16	GAMMA VOLTAGE 16	36	GIP_Reset	GIP Reset
12	GMA 18	GAMMA VOLTAGE 18 (Output From LCD)	37	VST	VERTICAL START PULSE
13	GND	Ground	38	VGL	GATE Low Voltage
14	LOCKOUT6	LOCKOUT6	39	VGH_EVEN	GIP Panel VDD for Even GATE TFT
15	LOCKIN3	LOCKIN3	40	VGH_ODD	GIP Panel VDD for Odd GATE TFT
16	NC	No Connection	41	VGI_P	GIP Bi-Scan (Normal =VGH Rotate = VGL)
17	VCC	Logic & EPI Power Voltage	42	VGI_N	GIP Bi-Scan (Normal =VGL Rotate = VGH)
18	GND	Ground	43	GCLK6	GIP GATE Clock 6
19	EPI6-	EPI Receiver Signal(6-)	44	GCLK5	GIP GATE Clock 5
20	EPI6+	EPI Receiver Signal(6+)	45	GCLK4	GIP GATE Clock 4
21	GND	Ground	46	GCLK3	GIP GATE Clock 3
22	GND	Ground	47	GCLK2	GIP GATE Clock 2
23	EPI5-	EPI Receiver Signal(5-)	48	GCLK1	GIP GATE Clock 1
24	EPI5+	EPI Receiver Signal(5+)	49	NC	No Connection
25	GND	Ground	50	LTD_OUT	LTD OUTPUT

Note : 1. Please refer to application note for details.
(GIP & Half VDD & Gamma Voltage setting)



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3-2-2. Backlight Module

[CN201]

1) LED Array ass'y Connector (Plug)

: SMH200-07

(black color, manufactured by Yeonho)

2) Mating Connector (Receptacle)

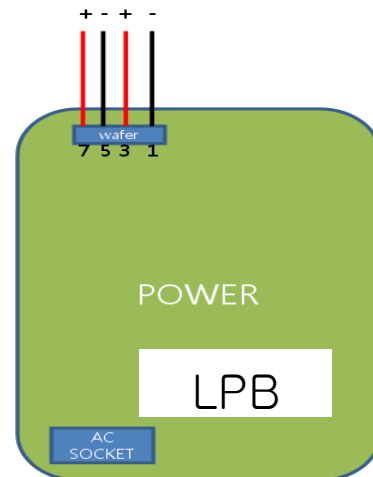
: SMAW200A-H07AA(Dip Type)

20037WR-H07AA(SMD Type)

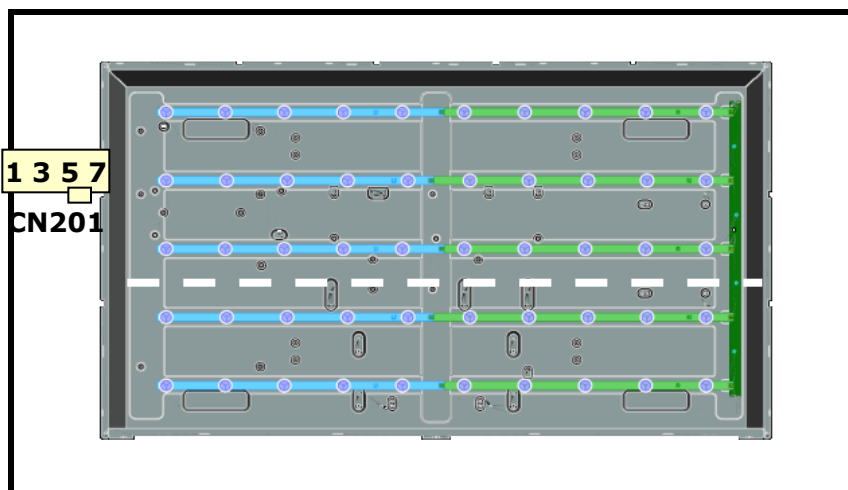
(black color, manufactured by Yeonho)

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN201)

No	Symbol(CN201)	Description
1	Cathode2	LED input Current
2	N.C	Open
3	Anode2	LED Output Current
4	N.C	Open
5	Cathode1	LED input Current
6	N.C	Open
7	Anode1	LED Output Current



◆ Rear view of LCM

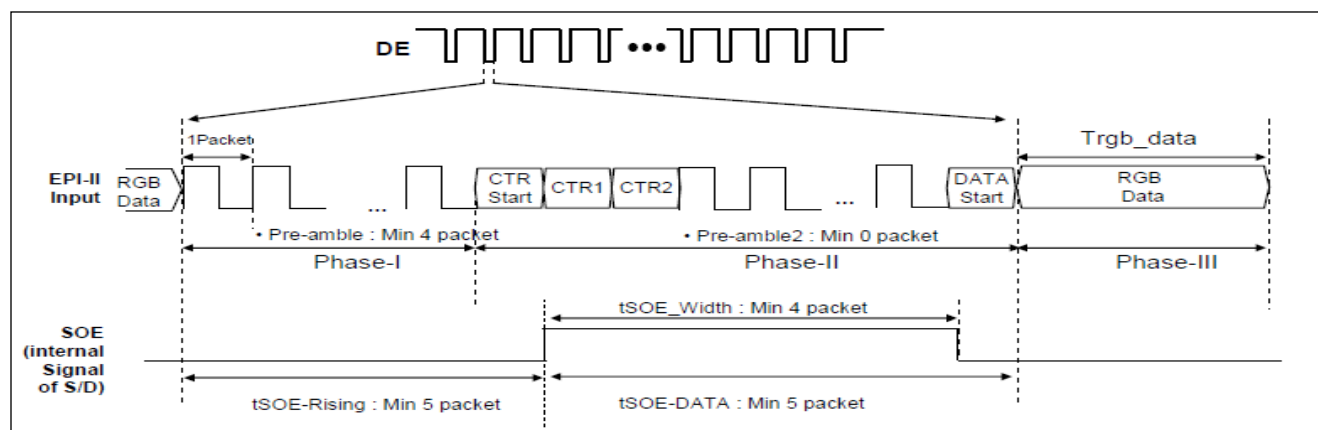


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3-3. Signal Timing Specifications

Table 4. Timing Requirements

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note
Unit Interval	UI	-	1.37	1.44	1.70	ns	
Effective Veye width time	B1&B2	-	0.25	-	-	UI	Fig. 2
SSC	Vspread	@100KHz	-	-	2	%	
Receiver off to SOE rising time	tSOE_ Rising		5	-	-	Packet	Fig4
SOE pulse width	tSOE_ Width	-	4	-	-	Packet	Fig.4
SOE rising to 1 st data time	tSOE_ DATA	-	5	-	-	Packet	Fig.4
EPI Bandwidth		-	0.588	-	0.728	GBPS	


FIG 4. SOE Width & Timing

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3-4. Panel Pixel Structure

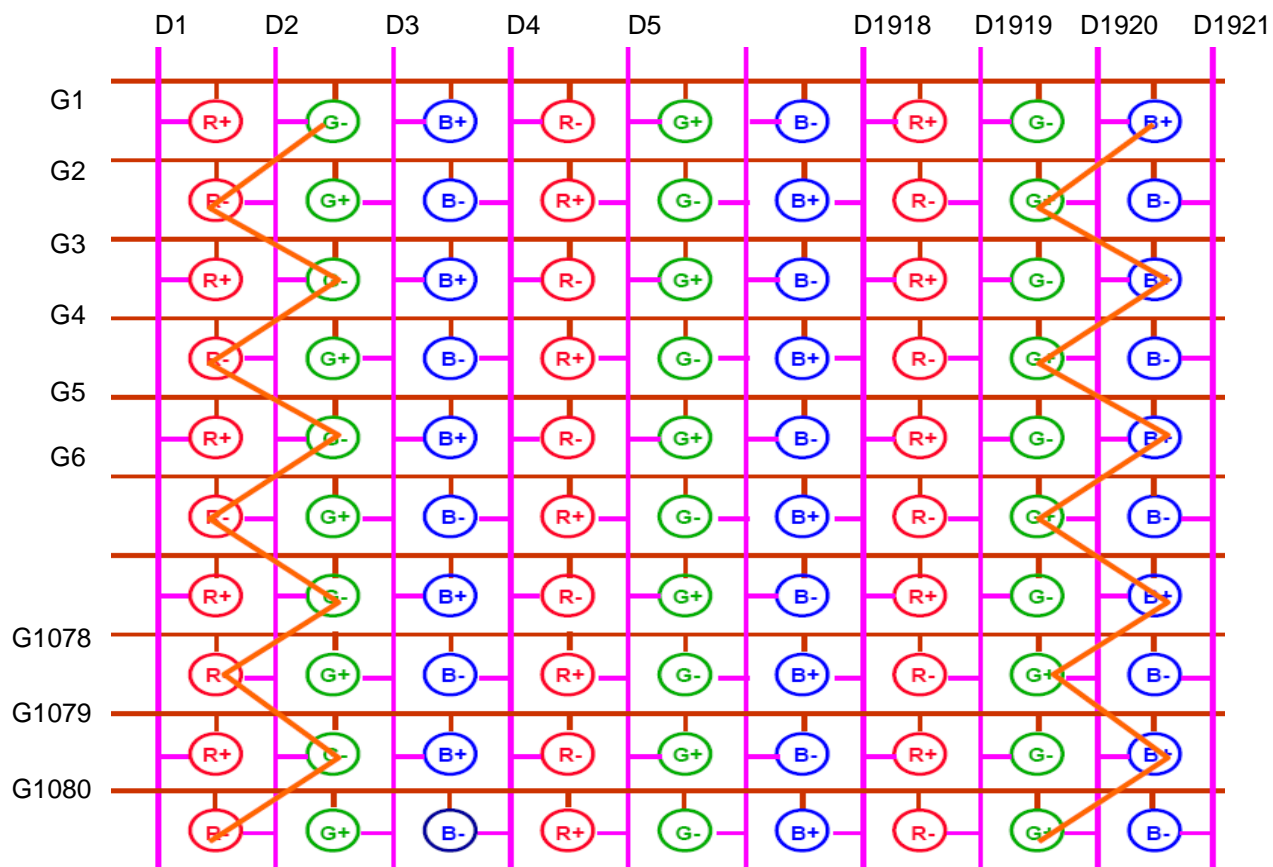


FIG. 6 Panel Pixel Structure

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3-5. Power Sequence

3-5-1. LCD Driving circuit

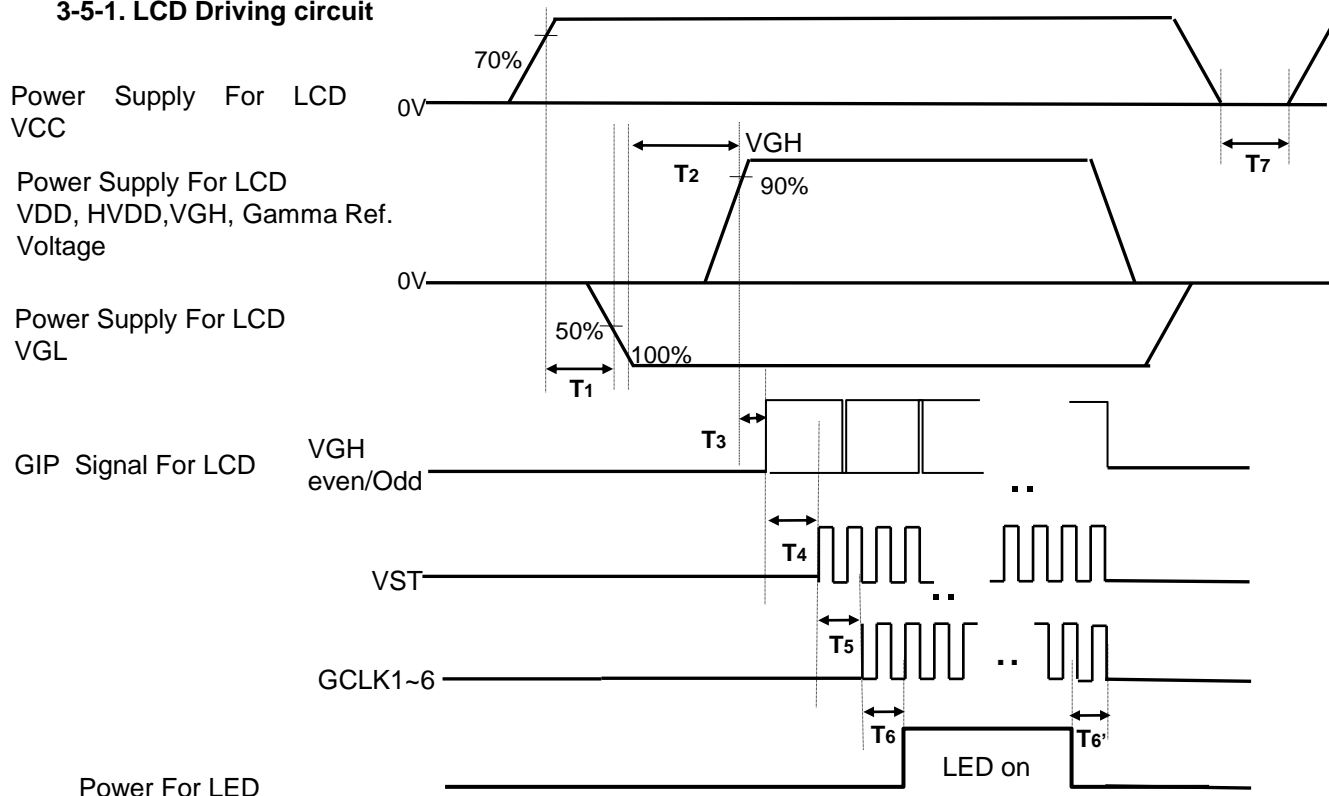


Table 9. POWER SEQUENCE

Ta= 25±2°C, fv=60Hz

Parameter	Value			Unit	Notes
	Min	Typ	Max		
T ₁	0.5		-	ms	
T ₂	0.5		-	ms	
T ₃	0		-	ms	
T ₄	10		-	ms	2
T ₅	0		-	ms	
T ₆ / T _{6'}	20		-	ms	6
T ₇	2		-	sec	

Note : 1. Power sequence for Source D-IC must follow the Case1 & 2.

※ Please refer to Appendix III for more details.

2. VGH Odd signal should be started "High" status and VGH even & odd can not be "High at the same time.

3. Power Off Sequence order is reverse of Power On Condition including Source D-IC.

4. GCLK On/Off Sequence

Normal : GCLK4 → GCLK5 → GCLK6 → GCLK1 → GCLK2 → GCLK3.

Reverse : GCLK3 → GCLK2 → GCLK1 → GCLK6 → GCLK5 → GCLK4.

5. VDD_odd/even transition time should be within V_blank

6. In case of T_{6'}, If there is no abnormal display, no problem

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at $25 \pm 2^\circ\text{C}$. The values are specified at distance 50cm from the LCD surface at a viewing angle of Φ and θ equal to 0° .

FIG. 8 shows additional information concerning the measurement equipment and method.

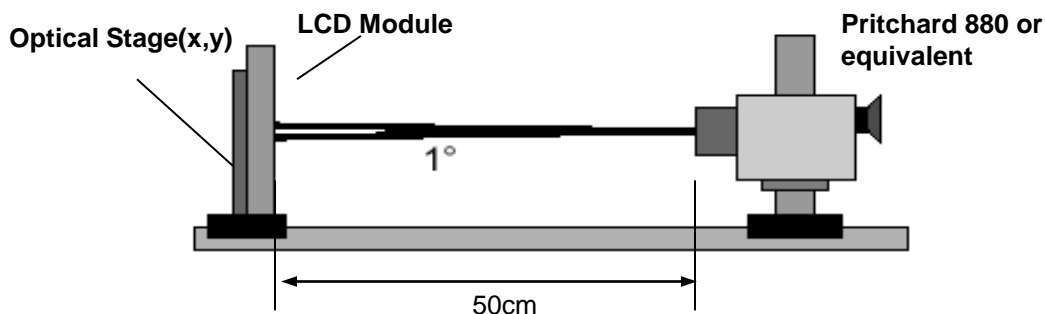


FIG. 8 Optical Characteristic Measurement Equipment and Method

$T_a = 25 \pm 2^\circ\text{C}$, $V_{\text{LCD}} = 12.0\text{V}$, $f_v = 60\text{Hz}$, $D_{\text{clk}} = 74.25\text{MHz}$,

$\text{EXTV}_{\text{BR-B}} = 100\%$

Table 10. OPTICAL CHARACTERISTICS

Parameter			Symbol		Value			Unit	Note
					Min	Typ	Max		
Contrast Ratio			CR		900	1200	-		1
Surface Luminance, white			L _{WH}	2D	240	300		cd/m²	2
Luminance Variation			δ _{WHITE}	5P			1.3		3
Response Time	Variation	G to G _σ		-	6	9			5
	Gray to Gray (BW)	G to G _{BW}		-	9	13	ms		4
Color Coordinates [CIE1931]	RED	Rx		Typ -0.03	0.649	Typ +0.03			
		Ry			0.333				
	GREEN	Gx			0.301				
		Gy			0.595				
	BLUE	Bx			0.149				
		By			0.061				
	WHITE	Wx			0.281				
		Wy			0.288				
Color Temperature						10,000	K		
Color Gamut						68	%		
Viewing Angle	2D (CR>10)	right(φ=0°)	θr (x axis)	89	-	-	degree	6	
		left (φ=180°)	θl (x axis)	89	-	-			
		up (φ=90°)	θu (y axis)	89	-	-			
		down (φ=270°)	θd (y axis)	89	-	-			
Gray Scale					-	2.2	-		7

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Note : 1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

It is measured at center 1-point.

2. Surface luminance are determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at $25 \pm 2^\circ\text{C}$. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 9.

3. The variation in surface luminance , δ WHITE is defined as :

$$\delta \text{ WHITE}(5P) = \text{Maximum}(L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5}) / \text{Minimum}(L_{on1}, L_{on2}, L_{on3}, L_{on4}, L_{on5})$$

Where L_{on1} to L_{on5} are the luminance with all pixels displaying white at 5 locations .

For more information, see the FIG. 9.

4. Response time is the time required for the display to transit from G(N) to G(M) (Rise Time, Tr_R) and from G(M) to G(N) (Decay Time, Tr_D). For additional information see the FIG. 10. ($N < M$)
5. G to G σ is Variation of Gray to Gray response time composing a picture

$$G \text{ to } G(\sigma) = \sqrt{\frac{\sum (X_i - u)^2}{N}}$$

X_i = Individual Data
 u = Data average
 N : The number of Data

6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 11.
7. Gray scale specification
Gamma Value is approximately 2.2. For more information, see the Table 11.

Table 11. GRAY SCALE SPECIFICATION

Gray Level	Luminance [%] (Typ)
L0	0.08
L15	0.27
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53.0
L207	63.2
L223	74.5
L239	86.7
L255	100

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Measuring point for surface luminance & measuring point for luminance variation.

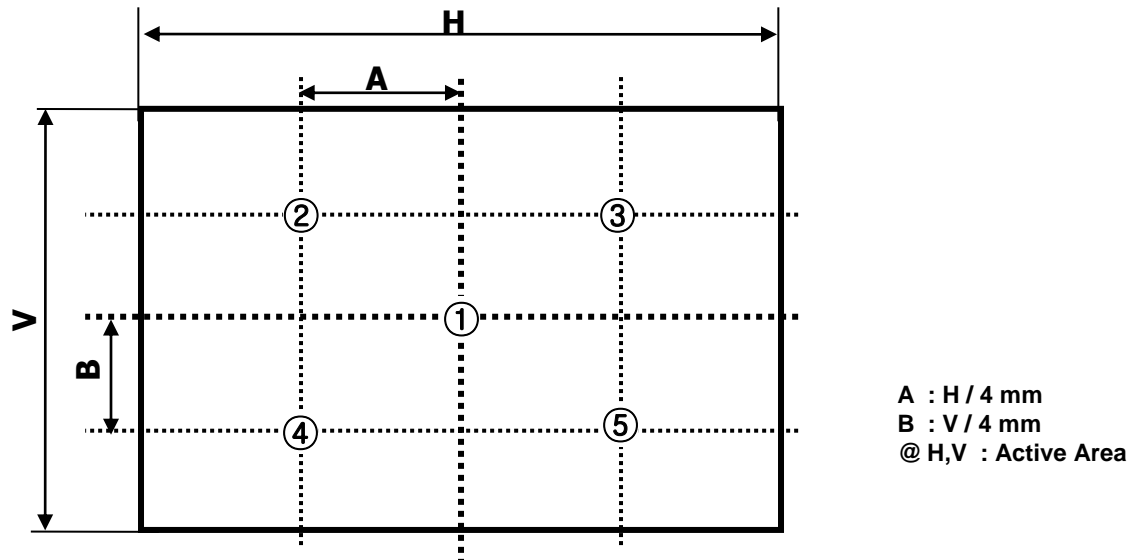


FIG.9 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for “Gray(N)” and “Gray(M)”.

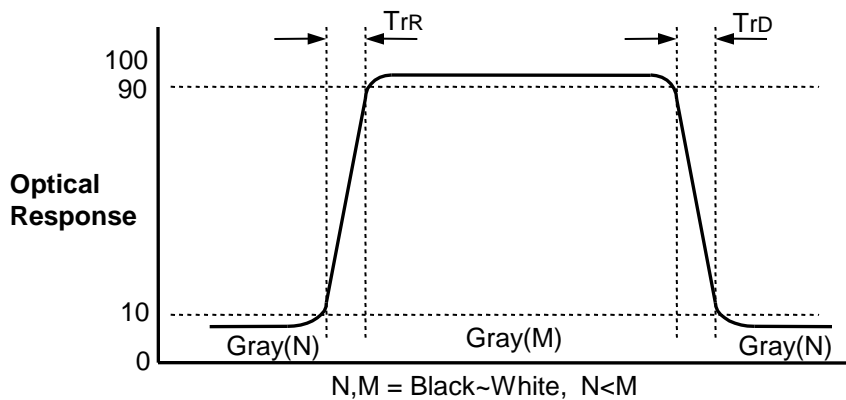


FIG. 10 Response Time

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Dimension of viewing angle range

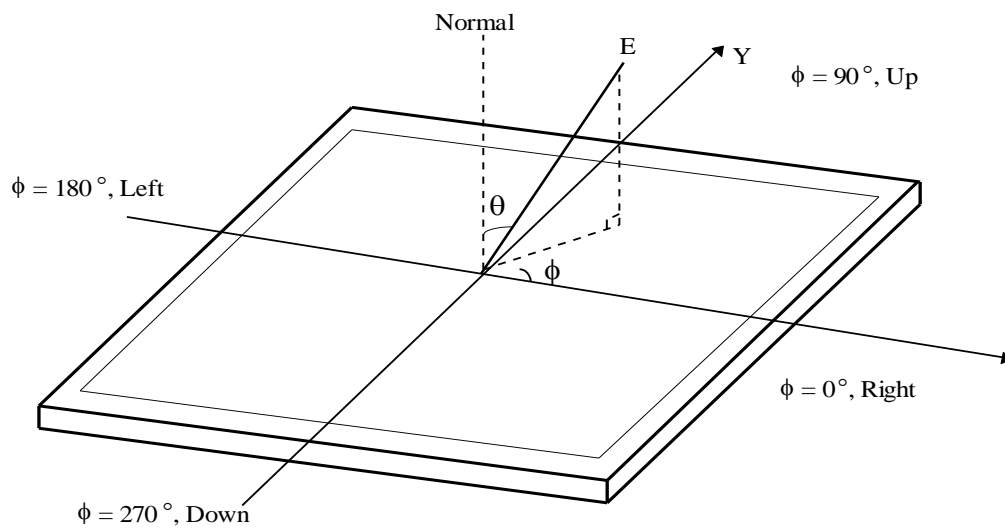


FIG. 11 Viewing Angle

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5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

Table 12. MECHANICAL CHARACTERISTICS

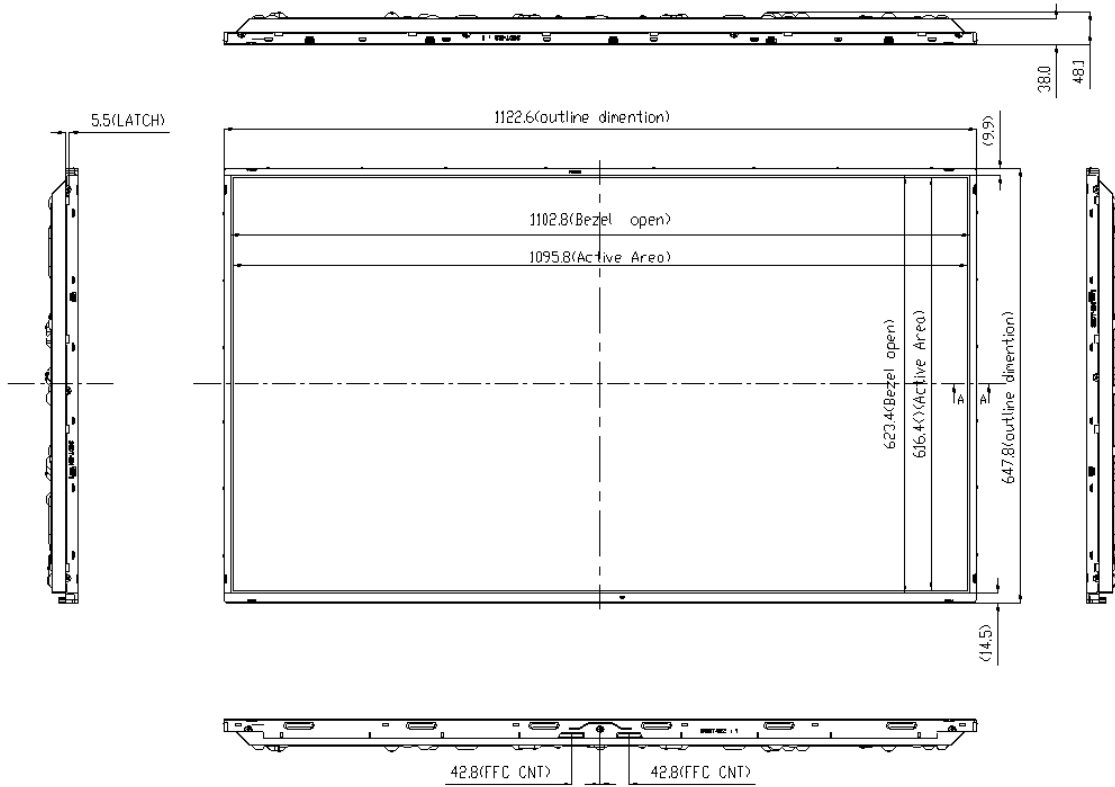
Item	Value	
Outline Dimension	Horizontal	1122.6 mm
	Vertical	647.8 mm
	Depth	38.0 mm
Bezel Area	Horizontal	1102.8 mm
	Vertical	623.4 mm
Active Display Area	Horizontal	1095.8 mm
	Vertical	616.4 mm
Weight	9.80Kg (Typ.), 10.0 kg (Max.)	

Note : Please refer to a mechanical drawing in terms of tolerance at the next page.

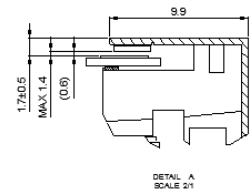
Engineering Specification

[FRONT VIEW]

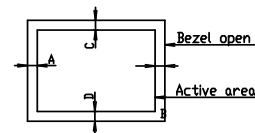
SET : TOP



SET : DOWN

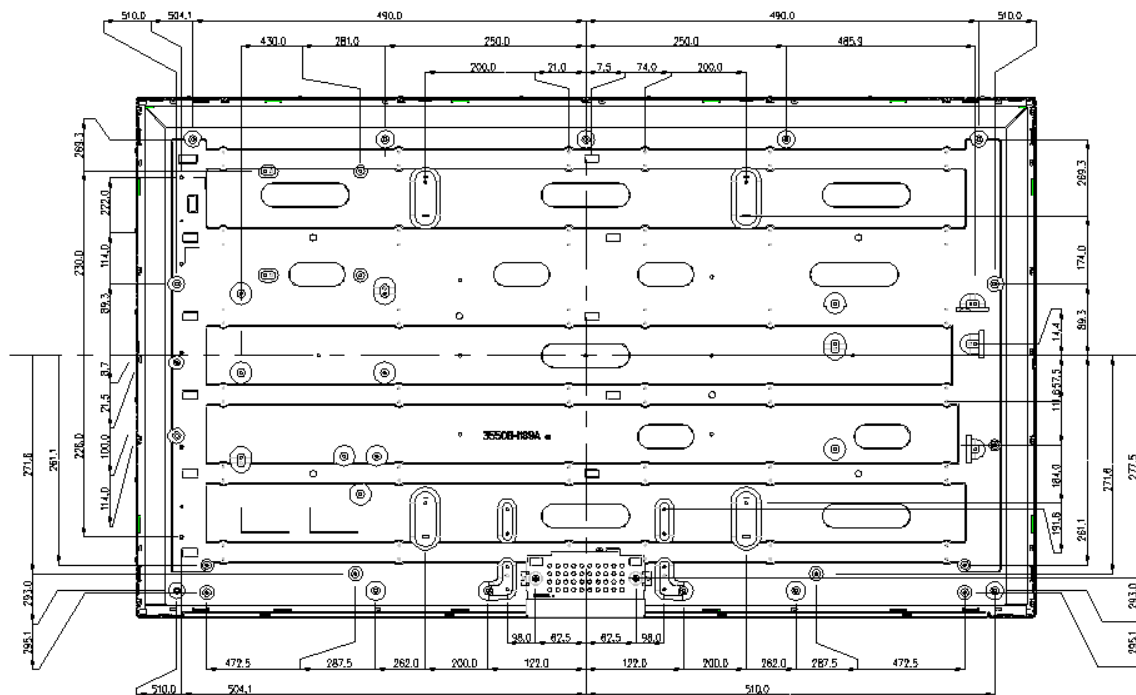


- NOTES
- 1.UNSPECIFIED TOLERANCE IS $\pm 1.0\text{MM}$
 - 2.TILT AND PARTIAL DISPOSITION TOLERANCE OF DISPLAY AREA AS FOLLOWING.
 - (1) Y-DIRECTION: $\angle A-B \leq 1.6$
 - (2) X-DIRECTION: $\angle C-D \leq 1.6$



[REAR VIEW]

SET : up



SET : down

Engineering Specification

6. Reliability

Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Humidity condition Operation	Ta= 40 °C ,90%RH

Note : Before and after Reliability test, LCM should be operated with normal function.

Engineering Specification

7. International Standards

7-1. LED Array - Safty

1. Laser (LED Backlight) Information

Class 1M LED Product
IEC60825-1 : 2001
Embedded LED Power (Class 1M)

2. Caution

: LED inside.

Class 1M laser (LEDs) radiation when open.

Do not open while operating.

7-2. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

Engineering Specification

8. Precautions

Please pay attention to the followings when you use this TFT LCD module.

8-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

8-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
 $V = \pm 200\text{mV}$ (Over and under shoot voltage)
- (2) Response time depends on the temperature. (In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
And in lower temperature, response time (required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
(if not, it can cause conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.

Engineering Specification

8-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

8-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

8-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.
It is recommended that they be stored in the container in which they were shipped.
- (3) Storage condition is guaranteed under packing conditions.
- (4) The phase transition of Liquid Crystal could be recovered if the LCM is released at the normal condition after the low or over the storage temperature.

8-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape.
When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

8-7. Operating condition guide

- (1) The LCD product should be operated under normal conditions. Normal condition is defined as below;
 - Temperature : 5 ~ 40 °C
 - Display pattern : continually changing pattern (Not stationary)
- (2) If the product will be used in extreme conditions such as high temperature, display patterns or operation time etc.,
It is strongly recommended to contact LGD for Qualification engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems. The LCD product should be applied by global standard environment. (refer ETSI EN 300, IEC 60721)

APPENDIX- I

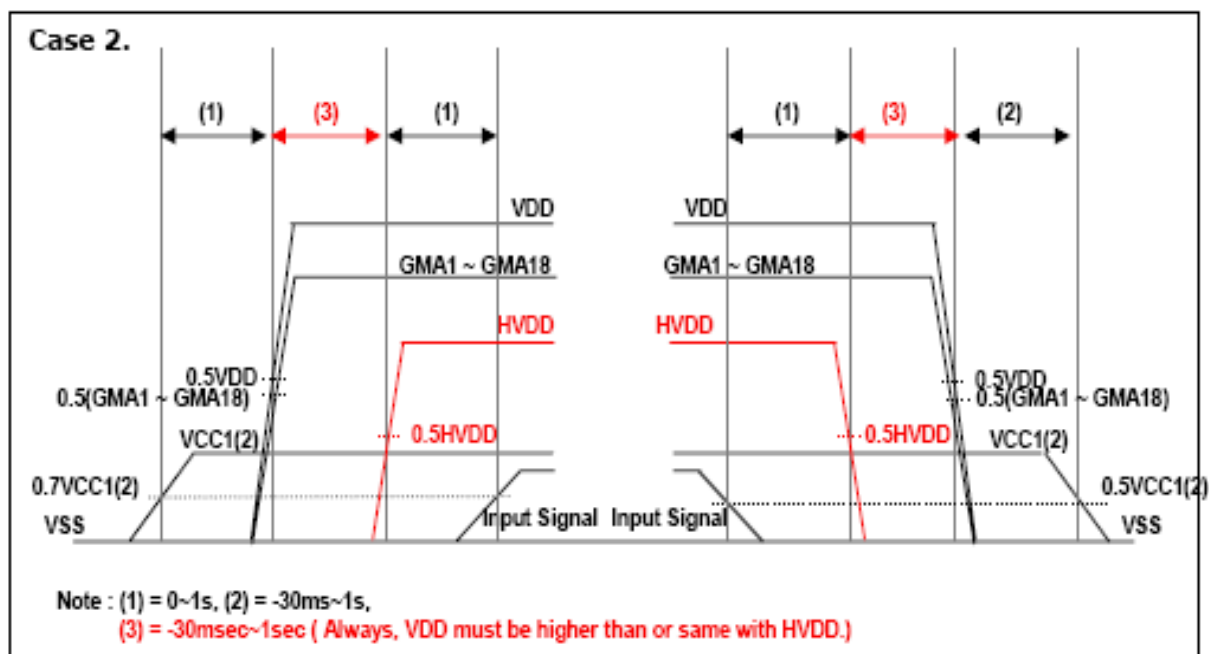
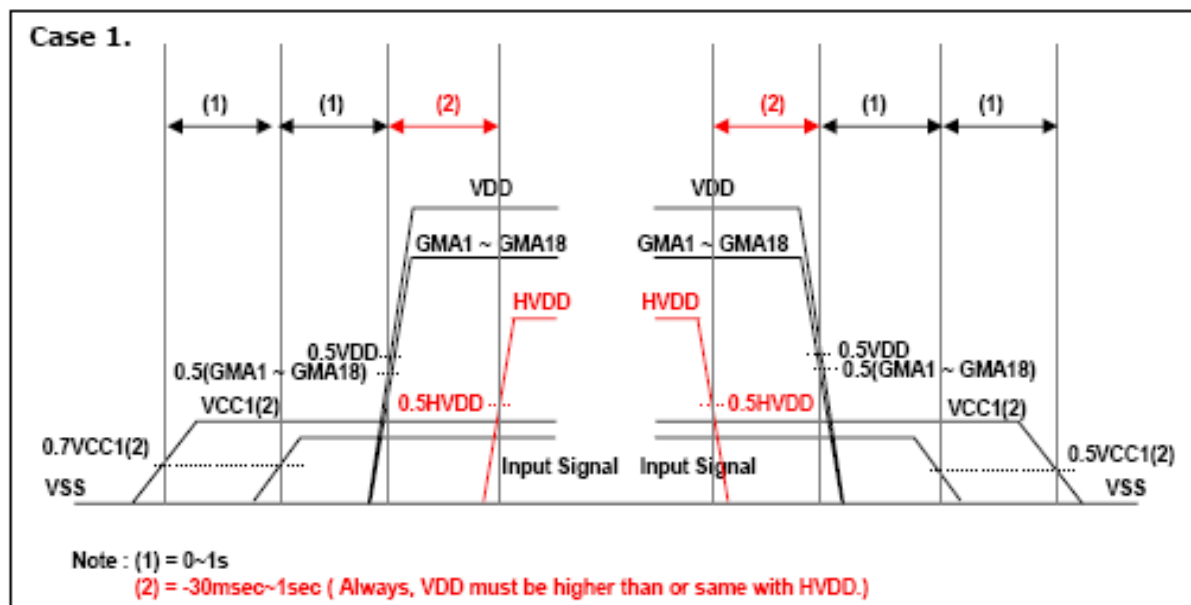
■ LCM Label



APPENDIX- I I

■ LCM Source power sequence

< Source power sequence >



- Input Signal : SOE,POL,GSP,H_CONV,OPT_N